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FACSIMILE COVER SHEET

DATE: July 5, 2005

TO: USPTO – Official Central Fax   FACSIMILE NO.: 703-872-9306
TELEPHONE:

FROM: Gary W. Hamilton
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SUBJECT: Application Serial No. 10/736,344 – Response to Non-Final Office Action

This transmittal consists of 6 page(s), including this cover sheet.

MESSAGE:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Yen-Chung T. Chen
Assignee:	Sun Microsystems, Inc.
Title:	Circuit for Reducing Current Mirror Mismatch Due to Gate Leakage Current

Serial No.:	10/736,344	Filed:	December 15, 2003
Examiner:	Khanh V. Nguyen	Group Art Unit:	2817
Docket No.:	SUN040072	Customer No.:	33438

Austin, Texas
July 5, 2005

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

RESPONSE TO NON-FINAL OFFICE ACTION

Sir:

This paper is responsive to the Office Action dated May 4, 2005, having a shortened statutory period expiring on July 4, 2005. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

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